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SHEET 1 OF 1

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION

(PTO-1449)

ATTY. DOCKET NO.
061352-0100SERIAL NO.
10/532,993APPLICANT
Michihito UEDAFILING DATE
April 28, 2005GROUP
Not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code(s) (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US			
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FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code(s)-Number(s) (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
JG		JP 48-102557 A	12/22/1973			partial only

OTHER ART (including Author, Title, Date, Relevance, Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		YAMANAKA et al.; "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals"; IEICE Trans. Electron.; c. 2001; Vol. E84, No. 12; pp. 1723 - 1729

EXAMINER
/James Goodley/DATE CONSIDERED
03/20/2008

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.



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/JG/		YAMANAKA et al.; "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals"; <i>IEICE Trans. Electron.</i> ; c. 2001; Vol. E84, No. 12; pp. 1723 - 1729

 EXAMINER
 /James Goodley/

 DATE CONSIDERED
 09/18/2008

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